

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 6,831,475 B2
APPLICATION NO. : 10/690496
DATED : December 14, 2004
INVENTOR(S) : Warren M. Farnworth et al.

Page 1 of 1

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

On the title page:

In ITEM (54) "Title",

change "DEVICE AND METHOD FOR
ISOLATING A SHORT-CIRCUITED
INTEGRATED CIRCUIT (IC) FROM
OTHER ICS ON A SEMICONDUCTOR
WAFER" to --DEVICE AND METHOD
FOR ISOLATING A SHORT-CIRCUITED
INTEGRATED CIRCUIT (IC) FROM
OTHER ICs ON A SEMICONDUCTOR
WAFER--

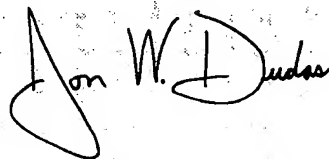
In the specification:

COLUMN 1, LINE 3,

change "(IC) FROM OTHER ICS ON A" to
--(IC) FROM OTHER ICs ON A--

Signed and Sealed this

Twenty-eighth Day of August, 2007

A handwritten signature in black ink, appearing to read "Jon W. Dudas", is written over a faint, rectangular official stamp.

JON W. DUDAS
Director of the United States Patent and Trademark Office